EAST Search History

Ref #	Hits	Search Query	th Query DBs Default Plurals Operator		Plurals	Time Stamp	
S2	5	(kvm near3 switch\$3) and (amplif\$6 near4 signal)	US-PGPUB; USPAT	OR	ON	2004/12/11 12:14	
S3	11	(kvm near3 switch\$3) and (emulat\$3 near6 signal)	US-PGPUB; USPAT	OR	ON	2004/12/11 12:19	
S6	2	(("6378614") or ("6633905")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/27 08:20	
S5	1	"6618774"	US-PGPUB; USPAT	OR	ON	2004/12/27 08:20	
S8	1	"6366951".PN.	USPAT; USOCR	OR	ON	2004/12/27 10:39	
S10	1	"6330594".PN.	USPAT; USOCR	OR	ON	2004/12/27 10:40	
S9	1	"6349330".PN.	USPAT; USOCR	OR	ON	2004/12/27 10:40	
S13	. 1	"6408351".PN.	USPAT; USOCR	OR	ON	2004/12/27 10:41	
S12	1	"6243738".PN.	USPAT; USOCR	OR	ON	2004/12/27 10:41	
S11	1	"6263373".PN.	USPAT; USOCR	OR	ON	2004/12/27 10:41	
S7	2	(("6378014") or ("6633905")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/27 10:48	
S15	0	("dustin.xa")	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 11:52	
S14	1	("6388658").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/27 11:52	
S17	0	"dustin.ax"	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 11:53	
S16	0	"dustin.xa"	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 11:53	
S19	17	dustin.xa.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 11:54	
S18	0	"dustin.px"	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 11:54	

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S20	5	S19 and parallel	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 11:55
S23	1	powerllel.as.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 11:56
S22	0	powerlell.as.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 11:56
S21	0	powerell.as.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/27 11:56
S24	35	kvm and (command near4 select\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/20 15:10
S27	1	"5968116".PN.	USPAT; USOCR	OR	ON	2006/01/20 16:02
S30	1	"5793999".PN.	USPAT; USOCR	OR	ON ·	2006/01/20 16:03
S29	1	"5913034".PN.	USPAT; USOCR	OR	ON	2006/01/20 16:03
S28	1	"5923103".PN.	USPAT; USOCR	OR	ON	2006/01/20 16:03
S31	1	"5051720".PN.	USPAT; USOCR	OR .	ON	2006/01/20 16:04
S32	12	"6557170"	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/20 16:05
S25	1	("6505245").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/20 16:05
S26	1	"6199180".PN.	USPAT; USOCR	OR	ON	2006/01/20 17:03
S34	1	("5016009").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/21 11:41
S33	1	("6618774").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/21 11:41
S35	1	("6754739").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/23 14:12
S1	2	"6671756"	US-PGPUB; USPAT	OR	ON	2006/01/23 14:12

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S37	2	(("6618774") or ("5937176")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/27 11:27
S38	1	("6571305").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/27 11:31
S36	6	"6618774"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/03/27 11:32
S39	1	("6654816").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/27 14:02
L1	1	("6345323").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/27 14:02
S4	6	(kvm near3 switch\$3) and (emulat\$3 and amplif\$6°and synchroniz\$6)	US-PGPUB; USPAT	OR	ON	2007/03/27 14:41
L3	23	(kvm) and (amplif\$6 and synchroniz\$6) and (signal near4 characteristic)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/03/27 14:41

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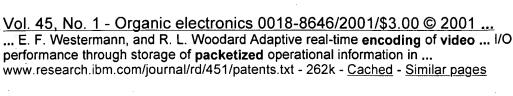
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KVDT = **Keyboard Video** Display Terminal. KVM = **Keyboard**, **Video**, Mouse. KW, kW = Kilowatt ... PACDAC = **PACketized** Digital to Analogue Controller/Converter ... www.ifatca.org/pdffiles/acronyms.pdf - <u>Similar pages</u>

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differential **amplifier**, fark yükselteci. differential **encoding**, ayrımsal kodlama, **... video** resolution, **video** çözünürlük. **video signal**, **video** işareti ... www.onokel.com/tamerdemir/technical-terms-dictionary.html - 956k - Cached - Similar pages

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Signal encode and split. Signal estimation and decode ... that unless VoIP on a packetized network will be available, wireless access would still lag behind ... www.itu.int/ITU-R/study-groups/docs/rwp8f-draft-tech-trends-report-en.doc - Similar pages

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The oscillator circuit which generates a periodic **synchronization signal**. ... token processing and supports circuit-switched voice and **packetized** data. ... www-ece.engr.ucf.edu/~jza/glossary/terms.html - 273k - <u>Cached</u> - <u>Similar pages</u>

MAC: Abbreviation for medium access control. machine-independent ...

A video display that is used to view a video signal to determine its image ... the part of the node that receives packetized traffic, depacketizes it, ...

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Cursor Control Keys, Kürsärne İdarä Töymäläre, Kürsär Yörtkeç Töymälär, ... Generic

Packetized Protocol, Paqetta tapşırunın ğomumi protoqoli ... tugan-tel.noka.ru/suezlek/en-tt.it?download - Similar pages

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SNR Signal to Noise Ratio. SOA Semiconductor Optical Amplifier ... CUU Cursor Up. CVF

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Research directions in virtual environments: report of an NSF Invitational Workshop,

March 23-24, 1992, University of North Carolina at Chapel Hill

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Gary Bishop, Henry Fuchs

August 1992 ACM SIGGRAPH Computer Graphics, Volume 26 Issue 3

Publisher: ACM Press

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A survey of research and practices of Network-on-chip

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Tobias Bjerregaard, Shankar Mahadevan June 2006 ACM Computing Surveys (CSUR), Volume 38 Issue 1

Publisher: ACM Press

Full text available: pdf(1.41 MB)

Additional Information: full citation, abstract, references, index terms

The scaling of microchip technologies has enabled large scale systems-on-chip (SoC). Network-on-chip (NoC) research addresses global communication in SoC, involving (i) a move from computation-centric to communication-centric design and (ii) the implementation of scalable communication structures. This survey presents a perspective on existing NoC research. We define the following abstractions: system, network adapter, network, and link to explain and structure the fundamental concepts. First, r ...

Keywords: Chip-area networks, GALS, GSI design, NoC, OCP, SoC, ULSI design, communication abstractions, communication-centric design, interconnects, network-onchip, on-chip communication, sockets, system-on-chip

Facial modeling and animation

Jörg Haber, Demetri Terzopoulos

August 2004 ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04

Publisher: ACM Press

Full text available: pdf(18.15 MB) Additional Information: full citation, abstract

In this course we present an overview of the concepts and current techniques in facial modeling and animation. We introduce this research area by its history and applications. As a necessary prerequisite for facial modeling, data acquisition is discussed in detail. We describe basic concepts of facial animation and present different approaches including parametric models, performance-, physics-, and learning-based methods. State-of-the-art techniques such as muscle-based facial animation, mass-s ...

3 A rate control scheme for adaptive real-time applications in IP networks with lossy links and long round trip times

Ian F. Akyildiz, Özgür B. Akan, Giacomo Morabito

June 2005 IEEE/ACM Transactions on Networking (TON), Volume 13 Issue 3

Publisher: IEEE Press

Full text available: 🔂 pdf(671.29 KB) Additional Information: full citation, abstract, references, index terms,

Currently there is no control for real-time traffic sources in IP networks. This is a serious problem because real-time traffic can not only congest the network but can also cause unfairness and starvation of TCP traffic. However, it is not possible to apply current solutions for Internet to the networks with high bandwidth-delay products and high bit error rates. The channel errors may result in inaccurate congestion control decisions and unnecessary rate throttles leading to severe performance

Keywords: flow control, high bandwidth-delay products, high bit error rates, real-time protocols

All-optical networks

Samir Chatterjee, Suzanne Pawlowski

June 1999 Communications of the ACM, Volume 42 Issue 6

Publisher: ACM Press

Full text available: pdf(296.73 KB) Additional Information: full citation, references, citings, index terms,

html(42.58 KB) review

Noncollaborative telepresentations come of age

D. James Gemmell, C. Gordon Bell April 1997 Communications of the ACM, Volume 40 Issue 4

Publisher: ACM Press

Additional Information: full citation, references, citings, index terms, Full text available: pdf(655.73 KB)

review

6 Challenge papers: Challenges: a radically new architecture for next generation

mobile ad hoc networks

Ram Ramanathan

August 2005 Proceedings of the 11th annual international conference on Mobile computing and networking MobiCom '05

Publisher: ACM Press

Full text available: pdf(273.07 KB) Additional Information: full citation, abstract, references, index terms

Despite decades of research and development, mobile ad hoc networks (MANETs) continue to lag behind wireline networks in terms of latency, capacity and robustness. We contend that a key reason for this is the way MANETs are thought about and architected today. We propose a radically new architecture that we believe will elevate MANETs to a performance plane on par with wireline networks. Our design concept for next generation MANETs is based on several revolutionary ideas - 1) a relay-oriented p ...

Keywords: ad hoc networks, medium access control, mobile, multi-hop, physical layer, relay, wireless

Reducing power while increasing performance with supercisc

Alex K. Jones, Raymond Hoare, Dara Kusic, Gayatri Mehta, Josh Fazekas, John Foster August 2006 ACM Transactions on Embedded Computing Systems (TECS), Volume 5 Issue

Publisher: ACM Press

Full text available: pdf(675.92 KB) Additional Information: full citation, abstract, references, index terms

Multiprocessor Systems on Chips (MPSoCs) have become a popular architectural technique to increase performance. However, MPSoCs may lead to undesirable power consumption characteristics for computing systems that have strict power budgets, such as PDAs, mobile phones, and notebook computers. This paper presents the super-complex instruction-set computing (SuperCISC) Embedded Processor Architecture and, in particular, investigates performance and power consumption of this device compared to tradi ...

Keywords: Low-power, VLIW, multicore architectures, predication, synthesis

8 Exploiting coarse-grained task, data, and pipeline parallelism in stream programs



October 2006 ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Architecture News , ACM SIGPLAN Notices , Proceedings of the 12th international conference on Architectural support for programming languages and operating systems ASPLOS-XII, Volume 40 , 34 , 41 Issue 5 , 5 ,

Publisher: ACM Press

Full text available: pdf(516.79 KB) Additional Information: full citation, abstract, references, index terms

As multicore architectures enter the mainstream, there is a pressing demand for high-level programming models that can effectively map to them. Stream programming offers an attractive way to expose coarse-grained parallelism, as streaming applications (image, video, DSP, etc.) are naturally represented by independent filters that communicate over explicit data channels. In this paper, we demonstrate an end-to-end stream compiler that attains robust multicore performance in the face of varying app ...

Keywords: Raw, StreamIt, coarse-grained dataflow, multicore, software pipelining, streams

9 Movie-maps: An application of the optical videodisc to computer graphics

Andrew Lippman

July 1980 ACM SIGGRAPH Computer Graphics , Proceedings of the 7th annual conference on Computer graphics and interactive techniques SIGGRAPH

'80, Volume 14 Issue 3

Publisher: ACM Press

Full text available: pdf(3.90 MB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms

An interactive, dynamic map has been built using videodisc technology to engage the user in a simulated "drive" through an unfamiliar space. The driver, or map reader, is presented with either sparsely sampled sequences of images taken by single frame cameras that replicate actual imagery from a space, or with computer synthesized replicas of those images. The reader may control the speed, route, angle of view and mode of presentation of this information and may thus tour the ar ...

Keywords: Anamorphic imagery, Computer generated imagery, Image processing, Interactive systems, Optical videodisc

10 Real-time shading

Marc Olano, Kurt Akeley, John C. Hart, Wolfgang Heidrich, Michael McCool, Jason L. Mitchell, Randi Rost

August 2004 ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04

Publisher: ACM Press

Full text available: pdf(7.39 MB)

Additional Information: full citation, abstract

Real-time procedural shading was once seen as a distant dream. When the first version of this course was offered four years ago, real-time shading was possible, but only with oneof-a-kind hardware or by combining the effects of tens to hundreds of rendering passes. Today, almost every new computer comes with graphics hardware capable of interactively executing shaders of thousands to tens of thousands of instructions. This course has been redesigned to address today's real-time shading capabili ...

11 Research directions in virtual environments: report of an NSF Invitational Workshop,



March 23-24, 1992, University of North Carolina at Chapel Hill Gary Bishop, Henry Fuchs

August 1992 ACM SIGGRAPH Computer Graphics, Volume 26 Issue 3

Publisher: ACM Press

Full text available: pdf(2.33 MB)

Additional Information: full citation, citings, index terms

12 The Direct3D 10 system



David Blythe

July 2006 ACM Transactions on Graphics (TOG), ACM SIGGRAPH 2006 Papers SIGGRAPH '06, Volume 25 Issue 3

Publisher: ACM Press

Full text available: 🔂 pdf(377.38 KB) Additional Information: full citation, abstract, references, citings, index mov(23:56 MIN) terms

We present a system architecture for the 4^{th} generation of PC-class programmable graphics processing units (GPUs). The new pipeline features significant additions and changes to the prior generation pipeline including a new programmable stage capable of generating additional primitives and streaming primitive data to memory, an expanded, common feature set for all of the programmable stages, generalizations to vertex and image memory resources, and new storage formats. We also describ ...

Keywords: graphics systems, programmable graphics hardware, programmable shading

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Program confinement in KVM/370

Marvin Schaefer, Barry Gold, Richard Linde, John Scheid

January 1977 Proceedings of the 1977 annual conference ACM '77

Publisher: ACM Press

Full text available: pdf(717.75 KB) Additional Information: full citation, citings

Executable JVM model for analytical reasoning: a study

Hanbing Liu, J Strother Moore

June 2003 Proceedings of the 2003 workshop on Interpreters, virtual machines and emulators IVME '03

Publisher: ACM Press

Full text available: pdf(230.18 KB)

Additional Information: full citation, abstract, references, citings, index terms, review

To study the properties of the Java Virtual Machine(JVM) and Java programs, our research group has produced a series of JVM models written in a functional subset of Common Lisp. In this paper, we present our most complete JVM model from this series, namely, M6, which is derived from a careful study of the J2ME KVM [16] implementation. On the one hand, our JVM model is a conventional machine emulator. M6 models accurately almost all aspects of the KVM implementation, including the dynamic class lo ...

Formalizing the safety of Java, the Java virtual machine, and Java card

Pieter H. Hartel, Luc Moreau

December 2001 ACM Computing Surveys (CSUR), Volume 33 Issue 4

Publisher: ACM Press

Full text available: pdf(442.86 KB)

Additional Information: full citation, abstract, references, citings, index

We review the existing literature on Java safety, emphasizing formal approaches, and the impact of Java safety on small footprint devices such as smartcards. The conclusion is that although a lot of good work has been done, a more concerted effort is needed to build a coherent set of machine-readable formal models of the whole of Java and its implementation. This is a formidable task but we believe it is essential to build trust in Java safety, and thence to achieve ITSEC level 6 or Common Crite ...

Keywords: Common criteria, programming

4 Special session on reconfigurable computing. The happy marriage of architecture



and application in next-generation reconfigurable systems Ingrid Verbauwhede, Patrick Schaumont

April 2004 Proceedings of the 1st conference on Computing frontiers CF '04

Publisher: ACM Press

Full text available: pdf(398.28 KB) Additional Information: full citation, abstract, references, index terms

New applications and standards are first conceived only for functional correctness and without concerns for the target architecture. The next challenge is to map them onto an architecture. Embedding such applications in a portable, low-energy context is the art of molding it onto an energy-efficient target architecture combined with an energy efficient execution. With a reconfigurable architecture, this task becomes a two-way process where the architecture adapts to the application and vice-vers ...

Keywords: embedded, real-time systems

5 Tuning garbage collection for reducing memory system energy in an embedded java





environment

G. Chen, R. Shetty, M. Kandemir, N. Vijaykrishnan, M. J. Irwin, M. Wolczko November 2002 ACM Transactions on Embedded Computing Systems (TECS), Volume 1 Teene 1

Publisher: ACM Press

Full text available: pdf(740.23 KB)

Additional Information: full citation, abstract, references, citings, index

Java has been widely adopted as one of the software platforms for the seamless integration of diverse computing devices. Over the last year, there has been great momentum in adopting Java technology in devices such as cellphones, PDAs, and pagers where optimizing energy consumption is critical. Since, traditionally, the Java virtual machine (JVM), the cornerstone of Java technology, is tuned for performance, taking into account energy consumption requires reevaluation, and possibly redesign of t ...

Keywords: Garbage collector, Java Virtual Machine (JVM), K Virtual Machine (KVM), low power computing

6 Sensor networks and performance analysis: Java™ on the bare metal of wireless



sensor devices: the squawk Java virtual machine

Doug Simon, Cristina Cifuentes, Dave Cleal, John Daniels, Derek White June 2006 Proceedings of the second international conference on Virtual execution environments VEE '06

Publisher: ACM Press

Full text available: pdf(999.55 KB) Additional Information: full citation, abstract, references, index terms

The Squawk virtual machine is a small Java™ virtual machine (VM) written mostly in Java that runs without an operating system on a wireless sensor platform. Squawk translates standard class file into an internal pre-linked, position independent format that is compact and allows for efficient execution of bytecodes that have been placed into a read-only memory. In addition, Squawk implements an application isolation mechanism whereby applications are represented as object and are therefore ...

Keywords: IEEE 802.15.4, Java virtual machine, Sun SPOT, embedded systems, wireless sensor networks

Compiling java for low-end embedded systems

Ulrik Pagh Schultz, Kim Burgaard, Flemming Gram Christensen, Jørgen Lindskov Knudsen June 2003 ACM SIGPLAN Notices, Proceedings of the 2003 ACM SIGPLAN conference on Language, compiler, and tool for embedded systems LCTES '03, Volume 38 Issue 7

Publisher: ACM Press

Full text available: R pdf(267.00 KB)

Additional Information: full citation, abstract, references, citings, index terms

The production of embedded systems is continuously increasing, but developing reusable software for such systems is notoriously difficult, in particular in the case of low-end embedded systems based on 16-bit or 8-bit processors. We have developed a compilation system for executing Java byte code on low-end embedded systems, and we demonstrate how this system permits object-oriented programming techniques to be used on devices with only a few hundred bytes of RAM and a few kilobytes of ROM. We an ...

Keywords: Java, compilers, embedded systems, interfaces

8 A taxonomy of computer program security flaws

Carl E. Landwehr, Alan R. Bull, John P. McDermott, William S. Choi September 1994 ACM Computing Surveys (CSUR), Volume 26 Issue 3

Publisher: ACM Press

Full text available: pdf(3.81 MB)

Additional Information: full citation, abstract, references, citings, index terms, review

An organized record of actual flaws can be useful to computer system designers, programmers, analysts, administrators, and users. This survey provides a taxonomy for computer program security flaws, with an Appendix that documents 50 actual security flaws. These flaws have all been described previously in the open literature, but in widely separated places. For those new to the field of computer security, they provide a good introduction to the characteristics of security flaws and how they ...

Keywords: error/defect classification, security flaw, taxonomy

Heap compression for memory-constrained Java environments

G. Chen, M. Kandemir, N. Vijaykrishnan, M. J. Irwin, B. Mathiske, M. Wolczko October 2003 ACM SIGPLAN Notices , Proceedings of the 18th annual ACM SIGPLAN conference on Object-oriented programing, systems, languages, and applications OOPSLA '03, Volume 38 Issue 11

Publisher: ACM Press

10

Full text available: pdf(2.14 MB)

Additional Information: full citation, abstract, references, citings, index

Java is becoming the main software platform for consumer and embedded devices such as mobile phones, PDAs, TV set-top boxes, and in-vehicle systems. Since many of these systems are memory constrained, it is extremely important to keep the memory footprint of Java applications under control. The goal of this work is to enable the execution of Java applications using a smaller heap footprint than that possible using current embedded JVMs. We propose a set of memory management strategies to reduce h ...

Keywords: Java virtual machine, garbage collection, heap, memory compression

Developing mobile 3D applications with OpenGL ES and M3G: Developing mobile 3D applications with OpenGL ES and M3G



Kari Pulli, Jani Vaarala, Ville Miettinen, Tomi Aarnio, Mark Callow July 2005 ACM SIGGRAPH 2005 Courses SIGGRAPH '05

Publisher: ACM Press

Full text available: pdf(9.22 MB)

Additional Information: full citation

11 Formal Models for Computer Security



Carl E. Landwehr

September 1981 ACM Computing Surveys (CSUR), Volume 13 Issue 3

Publisher: ACM Press

Full text available: pdf(2.98 MB)

Additional Information: full citation, references, citings, index terms

12 Poster session 2: A lightweight parallel java execution environment for embedded



multiprocessor systems-on-chip

Marco Mantovani, Simone Leardini, Martino Ruggiero, Andrea Acquaviva, Luca Benini March 2007 Proceedings of the 17th great lakes symposium on Great lakes symposium on VLSI GLSVLSI '07

Publisher: ACM Press

Full text available: 📆 pdf(388.80 KB) Additional Information: full citation, abstract, references, index terms

Java is a very popular execution environment for embeddedsystems platforms. However, current industrial and research implementation focus on single-processor platforms. In this paperwe analyze a prototype parallel Java Virtual Machine implementationtargeted to a symmetric multi-core architecture using sharedmemory communication. We focus on performance and energyanalysis, and we quantify the overheads of the virtual executionenvironment.

Keywords: Java, MPSoC

13 Ravenscar-Java: a high integrity profile for real-time Java



Jagun Kwon, Andy Wellings, Steve King

November 2002 Proceedings of the 2002 joint ACM-ISCOPE conference on Java Grande JGI '02

Publisher: ACM Press

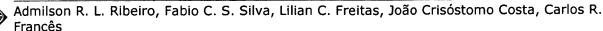
Full text available: pdf(175.79 KB)

Additional Information: full citation, abstract, references, citings, index

For many, Java is the antithesis of a high integrity programming language. Its combination of object-oriented programming features, its automatic garbage collection, and its poor support for real-time multi-threading are all seen as particular impediments. The Real-Time Specification for Java has introduced many new features that help in the real-time domain. However, the expressive power of these features means that very complex programming models can be created, necessitating complexity in the ...

Keywords: high integrity systems, profile, real-time Java

14 Multicast: SensorBus: a middleware model for wireless sensor networks



October 2005 Proceedings of the 3rd international IFIP/ACM Latin American conference on Networking LANC '05

Publisher: ACM Press



Full text available: pdf(124.39 KB) Additional Information: full citation, abstract, references

The use of middleware eases the development of distributed applications by abstracting the intricacies (communication and coordination among software components) of the distributed network environment. In wireless sensor networks, this is even trickier because of their specific issues such as addressing, mobility, number of sensors and energy-limited nodes. This paper describes SensorBus, a message-oriented middleware (MOM) model for wireless sensor networks based on the publish-subscribe paradi ...

Keywords: middleware, publish-subscribe paradigm, wireless sensor networks

15 Resolving covert channels within a B2 class secure system

Keith Loepere

July 1985 ACM SIGOPS Operating Systems Review, Volume 19 Issue 3

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings Full text available: pdf(1.15 MB)

For a secure computer system in the B2, B3 and A1 classes (as defined by the DoD Trusted Computer System Evaluation Criteria), the problem of confining a process such that it may not transmit information in violation of the *-property is an analyzable and solvable problem. This paper examines the problem of covert channels and attempts to analyze and resolve them relative to satisfying the B2 security requirements. A novel solution developed for the Multics computer system for a class of covert c ...

16 XTREM: a power simulator for the Intel XScale® core

Gilberto Contreras, Margaret Martonosi, Jinzhan Peng, Roy Ju, Guei-Yuan Lueh June 2004 ACM SIGPLAN Notices, Proceedings of the 2004 ACM SIGPLAN/SIGBED conference on Languages, compilers, and tools for embedded systems LCTES '04, Volume 39 Issue 7

Publisher: ACM Press

Full text available: pdf(1.07 MB)

Additional Information: full citation, abstract, references, citings, index terms

Managing power concerns in icroprocessors has become a pressing research problem across the domains of computer architecture, CAD, and compilers. As a result, several parameterized cycle-level power simulators have been introduced. While these simulators can be quite useful for microarchitectural studies, their generality limits how accurate they can be for any one chip family. Furthermore, their hardware focus means that they do not explicitly enable studying the interaction of different softwa \dots

Keywords: Java, XORP, XScale, hardware performance counters, power measurements, power modeling

17 Class analyses as abstract interpretations of trace semantics

Fausto Spoto, Thomas Jensen

September 2003 ACM Transactions on Programming Languages and Systems (TOPLAS), Volume 25 Issue 5

Publisher: ACM Press

Full text available: pdf(756.68 KB) Additional Information: full citation, abstract, references, index terms

We use abstract interpretation to abstract a compositional trace semantics for a simple imperative object-oriented language into its projection over a set of program points called watchpoints. We say that the resulting watchpoint semantics is focused on the watchpoints. Every abstraction of the computational domain of this semantics induces an abstract, still compositional, and focused watchpoint semantics. This establishes a basis for developing static analyses obtaining in

Keywords: Abstract interpretation, class analysis, denotational semantics

18 Identification of parallelism in neural networks by simulation with language J.

Alexei N. Skurihin, Alvin J. Surkan

September 1993 ACM SIGAPL APL Quote Quad, Proceedings of the international conference on APL APL '93, Volume 24 Issue 1

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(588.94 KB) terms.

Neural networks, trained by backpropagation, are designed and described in the language J, an APL derivative with powerful function encapsulation features. Both the languages J [4,6,7] and APL [5] help to identify and isolate the parallelism that is inherent in network training algorithms. Non-critical details of data input and derived output processes are deemphasized by relegating those functions to callable stand-alone modules. Such input and output ...

19 A framework for efficient reuse of binary code in Java

Pramod G. Joisha, Samuel P. Midkiff, Mauricio J. Serrano, Manish Gupta June 2001 Proceedings of the 15th international conference on Supercomputing ICS

Publisher: ACM Press

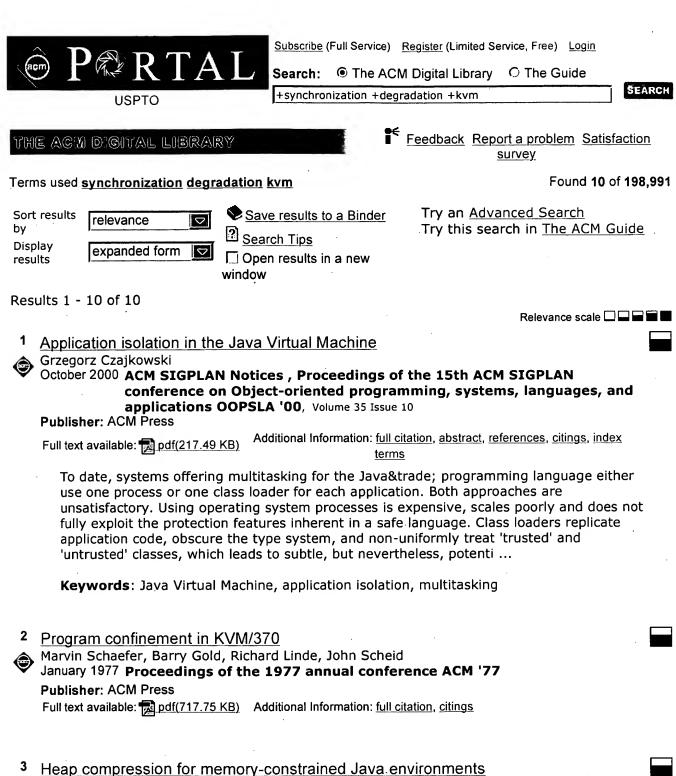
Full text available: 🔁 pdf(419.49 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a compilation framework that enables efficient sharing of executable code across distinct Java Virtual Machine (JVM) instances. High-performance JVMs rely on run-time compilation, since static compilation cannot handle many dynamic features of Java. These JVMs suffer from large memory footprints and high startup costs, which are serious problems for embedded devices (such as hand held personal digital assistants and cellular phones) and scalable servers. A recently propose ...

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G. Chen, M. Kandemir, N. Vijaykrishnan, M. J. Irwin, B. Mathiske, M. Wolczko October 2003 ACM SIGPLAN Notices, Proceedings of the 18th annual ACM SIGPLAN conference on Object-oriented programing, systems, languages, and

applications OOPSLA '03, Volume 38 Issue 11 Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(2.14 MB) terms

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of Java applications under control. The goal of this work is to enable the execution of Java applications using a smaller heap footprint than that possible using current embedded JVMs. We propose a set of memory management strategies to reduce h ...

Keywords: Java virtual machine, garbage collection, heap, memory compression

4 Objects and their collection: Exploiting frequent field values in java objects for

reducing heap memory requirements

Guangyu Chen, Mahmut Kandemir, Mary J. Irwin

June 2005 Proceedings of the 1st ACM/USENIX international conference on Virtual execution environments VEE '05

Publisher: ACM Press

Full text available: pdf(635.47 KB) Additional Information: full citation, abstract, references, index terms

The capabilities of applications executing on embedded and mobile devices are strongly influenced by memory size limitations. In fact, memory limitations are one of the main reasons that applications run slowly or even crash in embedded/mobile devices. While improvements in technology enable the integration of more memory into embedded devices, the amount memory that can be included is also limited by cost, power consumption, and form factor considerations. Consequently, addressing memory limita ...

Keywords: Java virtual machine, frequent field value, garbage collection, heap

5 Sensor networks and performance analysis: Java™ on the bare metal of wireless



sensor devices: the squawk Java virtual machine

Doug Simon, Cristina Cifuentes, Dave Cleal, John Daniels, Derek White
June 2006 Proceedings of the second international conference on Virtual execution
environments VEE '06

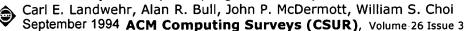
Publisher: ACM Press

Full text available: pdf(999.55 KB) Additional Information: full citation, abstract, references, index terms

The Squawk virtual machine is a small Java™ virtual machine (VM) written mostly in Java that runs without an operating system on a wireless sensor platform. Squawk translates standard class file into an internal pre-linked, position independent format that is compact and allows for efficient execution of bytecodes that have been placed into a read-only memory. In addition, Squawk implements an application isolation mechanism whereby applications are represented as object and are therefore ...

Keywords: IEEE 802.15.4, Java virtual machine, Sun SPOT, embedded systems, wireless sensor networks

6 A taxonomy of computer program security flaws



Publisher: ACM Press

Full text available: pdf(3.81 MB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms, review

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Keywords: error/defect classification, security flaw, taxonomy

7 A framework for efficient reuse of binary code in Java

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June 2001 Proceedings of the 15th international conference on Supercomputing ICS '01

Publisher: ACM Press

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8 Developing mobile 3D applications with OpenGL ES and M3G: Developing mobile 3D



applications with OpenGL ES and M3G

Kari Pulli, Jani Vaarala, Ville Miettinen, Tomi Aarnio, Mark Callow July 2005 ACM SIGGRAPH 2005 Courses SIGGRAPH '05

Publisher: ACM Press

Full text available: pdf(9.22 MB)

Additional Information: full citation

Multicast: SensorBus: a middleware model for wireless sensor networks



Admilson R. L. Ribeiro, Fabio C. S. Silva, Lilian C. Freitas, João Crisóstomo Costa, Carlos R. Francês

October 2005 Proceedings of the 3rd international IFIP/ACM Latin American conference on Networking LANC '05

Publisher: ACM Press

Full text available: pdf(124.39 KB) Additional Information: full citation, abstract, references

The use of middleware eases the development of distributed applications by abstracting the intricacies (communication and coordination among software components) of the distributed network environment. In wireless sensor networks, this is even trickier because of their specific issues such as addressing, mobility, number of sensors and energy-limited nodes. This paper describes SensorBus, a message-oriented middleware (MOM) model for wireless sensor networks based on the publish-subscribe paradi ...

Keywords: middleware, publish-subscribe paradigm, wireless sensor networks

10 A web-based system for dynamic teacher-student interaction in a classroom setting Robin M. Snyder

January 2007 Journal of Computing Sciences in Colleges, Volume 22 Issue 3

Publisher: Consortium for Computing Sciences in Colleges

Full text available: pdf(237.15 KB) Additional Information: full citation, abstract, references

This paper describes the design, implementation, and use of a web-based system for dynamic teacher-student interaction in a classroom setting. Since time is an inelastic resource and therefore precious in a classroom setting, the emphasis is on being able to easily and dynamically interact, document the interaction for future use, and improve the learning experience of the involved student. The specific scenario described in this paper is that of dynamically creating, documenting, publishing, an ...



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5. Implementation of MPEG system target decoder

Volume 2, 13-16 May 2001 Page(s):943 - 9946 vol.2 Digital Object Identifier 10.1109/CCECE.2001.933568

Electrical and Computer Engineering, 2001. Canadian Conference on

Azimi, M.; Nasiopoulos, P.; Ward, R.K.;

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□ ⁹	Proceedings of ICC/SUPERCOMM '96 - International Conference on Communication Communications, 1996. ICC 96, Conference Record, Converging Technologies for Toma Applications. 1996 IEEE International Conference on Volume 1, 23-27 June 1996 Digital Object Identifier 10.1109/ICC.1996.540233
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□ ¹	O. A variable bit rate layered DCT video coder for packet switched (ATM) networks Dixit, S.S.; Nardone, J.B.; Acoustics, Speech, and Signal Processing, 1990. ICASSP-90., 1990 International Confeon 3-6 April 1990 Page(s):2253 - 2256 vol.4 Digital Object Identifier 10.1109/ICASSP.1990.116019 AbstractPlus Full Text: PDF(440 KB) IEEE CNF
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